

İP³XÍÓ İŞB ÀŞÒà

ç0ÍÓĚ_çYİqÁaÔçDzÃÔ% À»»RŎ»Ã ÀfÀ ÀŞÒàĚ'ÃÔÂİĚ»R×ē×eŎ ÂaÑİÍ, ÂáÂSa[ÂİÀŞ
Ŏà»T



×ē%ƒ%İ: ESD (Electrostatic Discharge) ĤĚÚcŎ,,Ã
Ŏ,,%AN_»RçĚÃ ŬĖB Ŏ,,Ŏ (IC) ĀŏĚvÃ Â Â ÚcŎ,,Ña
ÚÍÃŎŎ%Be»RĀnĀŎÑ"ĀēÑ]ÍŚİ'ŎŎ»RŎēŎēŎ »RŬi çc
çu%eĀpç]ÃŎŎ†ç Â Â ÐaĚq»TĤĤ%WŎŠĀŎÚcŎ,,Ã Ŏ,,
ĀİÑ}ŶrİŎ% »R×ēŬ ĀĤ%ĤĀŎŎŠĀŎĚŎ»X

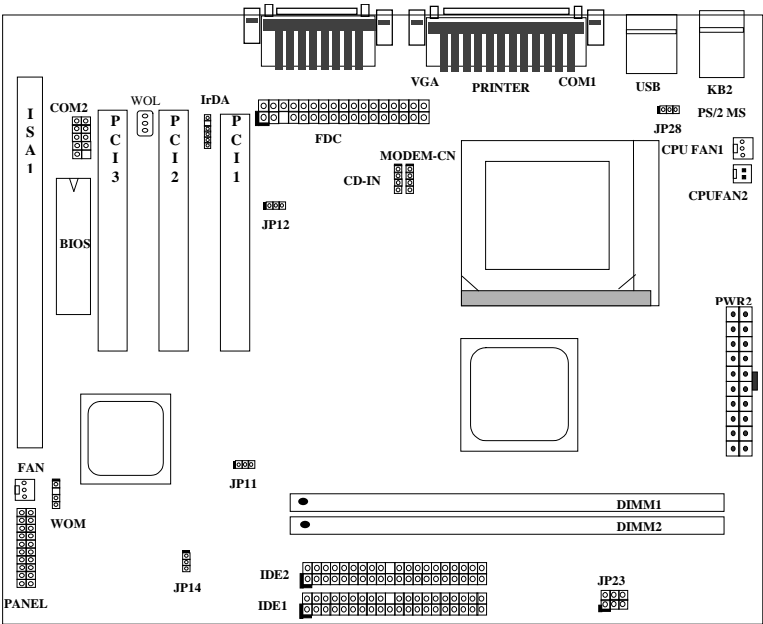
1. Ê%ĀŬĚ' %ŠÑĀÍĀĀĖÇĖĐ"Ā}ÀŞŎàŎ†ç »RĀpĀy%İŎf
%4ÇĖĚ_Ŏ†ç ĀŏĀŎŎcŎ,,çnŎà%3Ā çİ»T

2. ÀsĚ' ÀŞŎàŎ†ç Ěā»RÍæĀĖĚr%hĀÍĚİĀr×^ĀŎ%İ
Ŭ »R%4Í»ç ĀŎŎ,,%4Ŏ†ç À Í%ĀÍĀs× »TĀfĀXĀdĀÍ
%İŬ »R×ēĀēçèç Ā çzçYĀŎ% ÚcŎ,,ĀŎİŬç_Ā•»RĀİ
ŎaĀİĚŎ†ç %eçUŎ Ā`Ěİpİ»T

İŞB ÅŠ0à

2.1 Jumper 0aËİÚj ÄÔÀ 0~

¿Y¼Æ¿U0 Ä`¼Jumper Ä^ ËİÚj (connector) ÄÔË0~ 0é»X



İŞB ÅŠòà

Jumpers:

JP11:	Èì ¿è/Ý ÍÆÔÀð AGP
JP12:	Èì ¿è/Ý ÍÆÔÀðÇİÈÞİÓ%
JP14:	İ^ÊCMOS
JP23:	AGP Ratio
JP28:	Üþ×]/ÑàÓÅ"Ø

İ†ËİÜj :

PS2:	PS/2 ÑàÓÀËİÜj
KB:	PS/2 Üþ×]ËİÜj
COM1:	COM1 ËİÜj
COM2:	COM2 ËİÜj
PRINTER:	À]Ã Ø ËİÜj
PWR2:	ATX Ó„Ñ×ËİÜj
USB:	USB ËİÜj
FDC:	Í€0€Ø ËİÜj
IDE1:	ÌÞ%QÌiIDE İ†ËİÜj
IDE2:	ÌÞ%XÌiIDE İ†ËİÜj
CPUFAN1:	CPU ÇÑÈÈÈİÜj
CDUFAN2:	CPU ÇÑÈÈÈİÜj
FAN:	Ø ÎàÇÑÈÈÈİÜj
IrDA:	IrDA (Æ ¿•×^) ËİÜj
PANEL:	Åv¼ ÇÈÄ`Å Üþ0aÜ`ð0Í†ËİÜj
CD-IN:	CDROM ÇİÑ××^ËİÜj
MODEM-CN:	Mono in (Pin 1-2) 0a Mic out (Pin 3-4)
WOM:	0V Wake On ModemËİÜj
WOL:	Wake On LAN ËİÜj

İŞB AŞÖa

2.2 Jumpers

İ, D ÇU0 Ä`Æ ÈPçèİ jumper İncf»RÈu%QÄ0 jumper Æ İ`È CMOS»RÄpçUÇÈÆ Çè Ä ÄeÈ` È\`xi ÄÖÈäÇi»T

2.2.1 Ü Â CPU Úhİ%

İ, D ÇU0 Ä` ÇÇYÄöÈäÈÖİ CPU Ó„Ú%»R ÄYÇSÇÇYB È`Äs CMOS setup %4İmÄŞ CPU Úhİ%»RÄİİ]DNÄéçèÄ jumper»Tçç•»R%4vçYÍ`Ó] %4ÄYİ`Ä»RÈ_çÜ×eÄ0 CPU ò ÈeÈ` Ü Â EEPROM %4»TÄfÄ0%QÄİ»R0f%Q MOS Ü Ç%4W»RÈ` Î_ %4%è0ü%4ÇÄİn CPU Ó„Ú%Èäçi %4WÈÜ »R%4ß4%çİÄÉChÄ0çİD"Ó„0%0 İuÚaİ CPU ÖöY %4W»TÄİİ, %4Æ %QÉ ÈPçèİ jumper İncfÄ0 Pentium ÇU0 Ä` ÇUÇÈÄÖÈ÷YU»T

İmÄŞ CPU Úhİ%Ä0% Ä|Æ »X

BOIS Setup à Chipset Features Setup à CPU Clock Frequency

(The possible setting is 66.8, 75, 83.3, 100, 103, 105, 110, 112, 120, 124, 133, 140, 150 MHz)

BOIS Setup à Chipset Features Setup à CPU Clock Ratio

(çzÈuÄ0İnÄŞÇaÄİ 1.5x, 2x, 2.5x, 3x, 3.5x, 4x, 4.5x, 5x, 5.5x, 6x, 6.5x, 7x, 7.5x, 8x)

Core frequency = Ratio * External bus clock

INTEL Celeron PPGA	CPU Core Frequency	Ratio	External Bus Clock
Celeron PPGA 300A	300MHz=	4.5x	66MHz
Celeron PPGA 333	333MHz=	5x	66MHz
Celeron PPGA 350	350MHz=	3.5x	100MHz
Celeron PPGA 366	366MHz=	5.5x	66MHz
Celeron PPGA 400	400MHz=	6x	66MHz
Celeron PPGA 433	433MHz=	6.5x	66MHz



PIÄz: INTEL 440ZX İÖ% İiİaEÄçz%PIÄ 100MHz FSB»RÄp0}DaÄe%Ö İ»İ Öiçè»Tİ, ÄaİnÄŞÇa%ŞDhçiİÖ% İiÄ0İhÈ »RçzÈuN"Ó ÄİİeİGAÄN} Èq»T

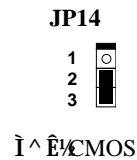
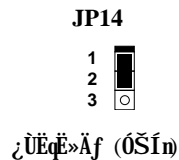
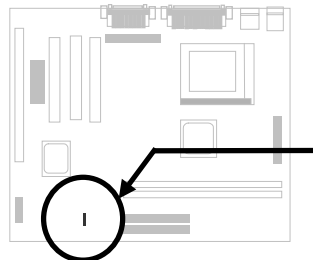
2.2.2 CPU Ó„Ú½

çŒçU0 Å` %pİÃCeleron PPGA VID çŒú»RçzÀõÊãÊÕİ CPU Èð%úÓ„Ú½»RÂp×uÍ Às 1.3V Å 2.05V %ÁĐ»»T

2.2.3 İ^Ê½CMOS

JP14	İ^Ê½CMOS
1-2	çŒËç»Äf (ÓŠİn)
2-3	İ^Ê½CMOS

ÀfÅXÈ‘ ÁeÈ` Äi İnÃŠA0ÄİİeÈ\×i Èã»RçİĐŒç È_ [aBEİ İnÆİ C>Đ»Rİ^ È½ÄYÇÄİm T^` dİ Ä0İnÃŠçã Äû»R%ÉúÇÄŒİĐ”Ø »T



İ^Ê½CMOS Ä0İ‘ Áá:

1. Ý İÄİİe0„Œ×»T
2. ÄoËÖ PWR2 %ÄÄ0ÄTX Ó„Œ××^»T
3. Äpçİ JP14 ÄİÀsÄ0Ä 0~»RË_Đ ×ÄİfÖÄ %fÄİ»RÄ È^Đ-3 0”Ä %Ä»T
4. %QŒÄüÊãĐ»Äü»RÄ %fĐ ×ÄİfÖÄÄŒİÈ^ ÄoÄ 1-2 0”Ä %Ä»RÄ İpÄÄÇ ÄİÄ0ÄÆ ÖR»T
5. È_ÄTX Ó„Œ××^ËİÄo PWR2»T
6. ÇÄŒİçİĐ”Ö„0¼Ö„Œ×»T
7. ÀfÅXŒbÇ€İnÃŠŒÄ0ÄİİeÈ\×i »RçzÀsÄİİeÈİ ÊãÊã»RÄ %f [DEL] ÜpĐz% BIOS Setup İvÇË%ÄRÄÄ ÄŠŒİÄ0È\×i »T



İ½ö: ÀfÅXÈ‘ Ä0ÄİİeÄnÆĐhÜhÄİ Œ ÈÖÄeİ|Ä|Đ”Ø »RçzçYİ^Ê½ CMOS»RB ÄİİeÄoÄ ÓŠİnÄ0ÄÆÖR»T

İ½ö: È½ÄÆéè JP14 %Äç»RË‘ %nçzçYçè <Home> ÜpÄİİ^Ê½CMOS»T %Ä|Æ Ä Ä <Home> Üpİ^ÄüĐ”ÈİÓ„Œ×Đ”Ý »Rİ, ÖaÄİİeİ_Œ”ÄoÊãÊ_ CPU

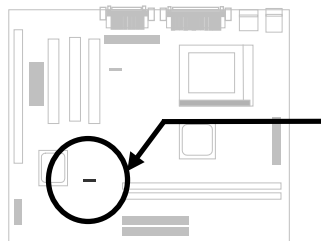
İŞB ÅŠ0à


İnÅÊ 300MHz»TÊİ¼fÂİ»RÊ' çzÀRÂæÓ ÖöÄÄf»RDz¼ BIOS Setup İnÅŠ CPU
Úhİ¼»T


2.2.4 AGP Đ"Ý

JP11	AGP Đ"Ý
1-2	Enabled (default)
2-3	Disabled

Çj Ê' ¼ÄbÂéçè%ÔÂò AGP»RççYË_ÀÓ jumper
İnÅÊ Disabled»T



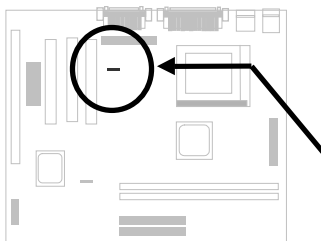
JP11
1 2 3

Enabled (default)


JP11
1 2 3

Disabled


2.2.5 ÇİËPİÓ¼ Đ"Ý

JP12	Onboard Audio
1-2	Enabled (default)
2-3	Disabled

Çj Ê' ¼ÄbÂéçè%ÔÂòÇİËPİÓ¼ »RççYË_ÀÓ
jumper İnÅÊ Disabled»T



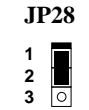
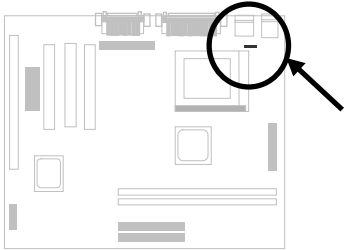
JP12
1 2 3

Enabled (default)

JP12
1 2 3

Disabled

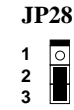
2.2.6 KB/MS Wakeup

JP28	KB/MS Wakeup
1-2	Disabled
2-3	Enabled

çèÁí Èì çèÛp×|/ÑàÓÁÐ"Ø çñú»TÇj ÍnÆ Enabled»R
Ë' ÛóÐÑË,, BIOS Setup ¼¼ÍnÁŠÐ"Ø ÕìÀ»»TÇ€Áéçè
ÀÓçñú»R5V Stand By Ó,,ÆË ÌÐÑ¼Ä 800mA»RÁì
çYÆ ÅáÓ,,Ñ×ØÓçÆúÑ"Í|Ä|Áéçè»T
×èÄqÑ_»Rç^ÀÍ PS/2 ÑàÓÁ¼pÌÁÑàÓÁÐ"Ø »T



Disabled



Enabled

İŞB AŞÖa

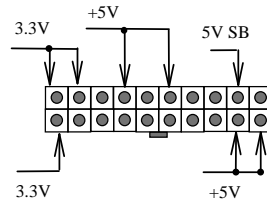
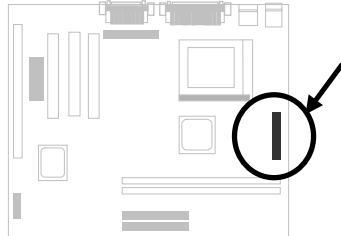
2.3 İ†ËİÚj

2.3.1 Ó„Ñ×Í†Ëİ×^

ATX Ó„Ñ×Í†Ëİ×^ 20-pin İ†ËİÚj »R×ê×eÄŞË'İ»%4Ä0% ÄgÆ ÇÜ×eÄ0»T



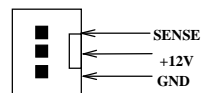
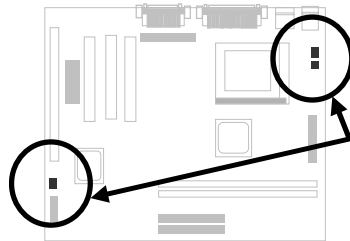
×ê×eÄŞË'İ: ÄsÍ†ËİÄeÄ0E%Ó„Ñ×Í†Ëİ×^%ÄÄv»R×ê Ç Ý ÍÄ†İè
Ó„Ñ×»T



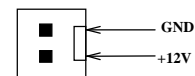
PWR2

2.3.2 ÇÑËË

ÄsÇU0 Ä`%4»RAÍ%ÇÇİöçöÄ»CPUFAN1 öa CPUFAN2 Ä0 CPU ÇÑËËİÚj »RÖa%Q
ÇİöçöÄ»FAN Ä00 İuÇÑËËİÚj »T



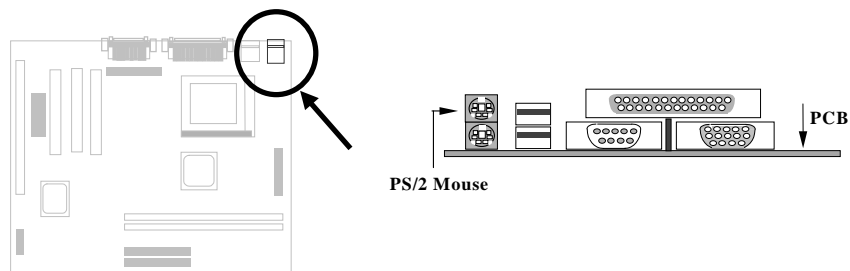
CPUFAN1 & FAN



CPUFAN2

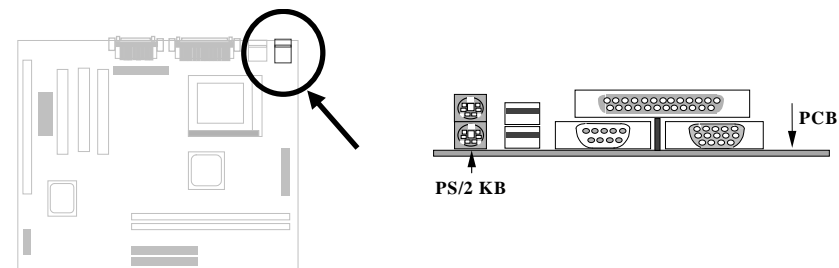
2.3.3 PS/2 ÑàÓÀ

×éÍ†ËÏ PS/2İÑàÓÀÀ Òë¿öÆÏPS2İÄÖËÏÚj ¼h»T



2.3.4 Ûp×]

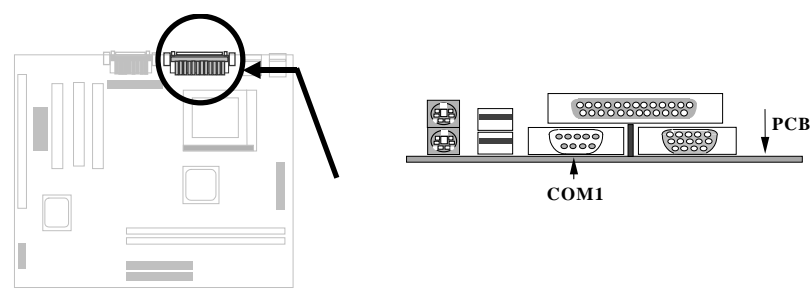
×éË_PS/2 Ûp×]ËÏÄ Òë¿öÆÏKB ÄÖÍ†ËÏÚj ¼h»T



İŞB ÅŠ0à

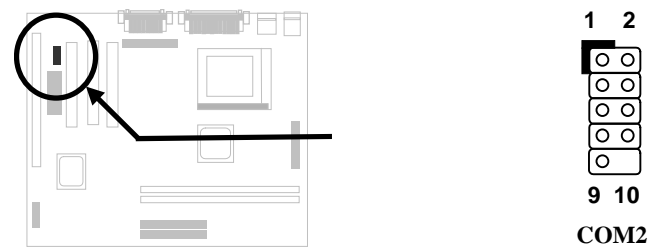
2.3.5 À ÀTÊ (COM1)

Åü% ÇËÄ` %4ıÅİ%QÇiÖë¿ðÆ COM1 ÅÖ 9-pin D-ÅÆ ËİÜj »R¿¿ëÅİİ†ËİÀ ÀTÊ ÑàÓÅ
(serial mouse) ÅëÆ 0à000 »T



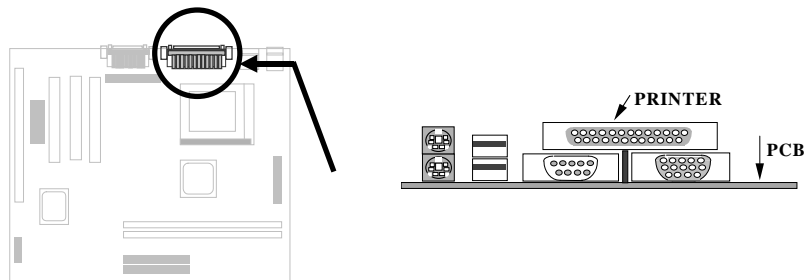
2.3.6 À ÀTÊ (COM2)

×ëË_ 10-pin ÅÖËà×`ËİÅö COM2»T



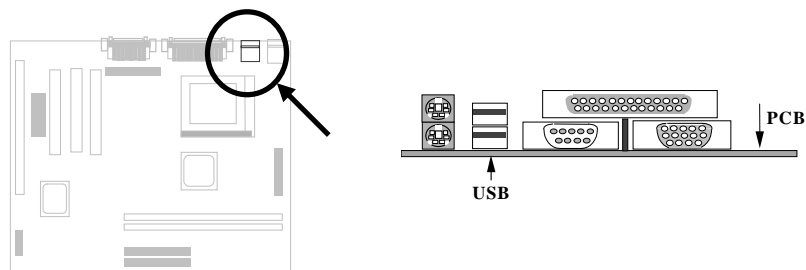
2.3.7 À]Ä Ø

çUØ Ä` Äü% ÇÄÄ` %hÄÍ%QÇiÖêö~~PRINTER~~ ÄÖ 25-pin D-ÄËÜj »RçèÄíÄŠİ»ÄÝÄT
Ä»Ä]Ä Ø »T



2.3.8 USB òàò~

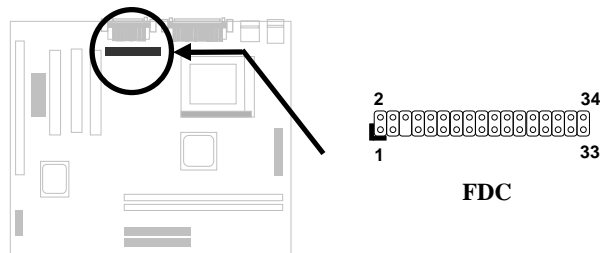
Ë`çË_ USB òàò~Í†ËİÄ USB ËİÜj »RÍ, D çUØ Ä` %hÄÍÄüQJUSB ËİÜj »RÖêçöÄË
USB»T



İŞB AŞÖa

2.3.9 İ€0ê0

AszU0 Ä` %hÄÍ%QÇiÖëzöMDC Ä0 34-pin EİÜj »RzZëÄÍİ†EİÄüç<İ€0ê0 »T

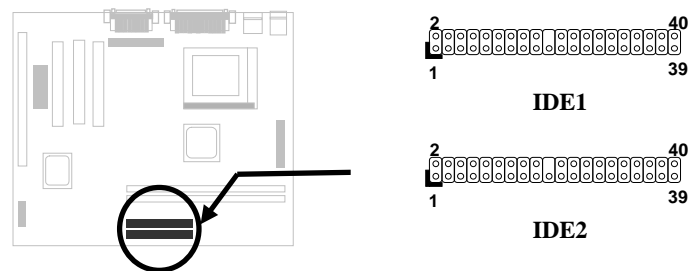


2.3.10 IDE İŞÖê0 Öa CDROM

AszU0 Ä` %h»RÊİÄİÄüÇiÖëzöMIDE1 Ä^ IDE2 Ä0 40-pin ÈaÊS»RzZÄÍİ†EİÄüÇi
IDE 0a0~»RİæÄyçzİ†Eİç“Çi IDE 0a0~»R%QÉ IDE1 %d06ÆçUÍ„ÖU (primary
channel)»RIDE2 %d06ÆÄ0Í„ÖU (secondary channel)»T

İ†EİÄ ç %QÍ„ÖUÄ0İP%Qç<0a0~çİĐNİnMmaster mode»WİP%Kç<0a0~çİĐNİnM
slave mode»Tç %QÇi0a0~Ä»çÆİŞÖê0 Äèç 0ê0 »T

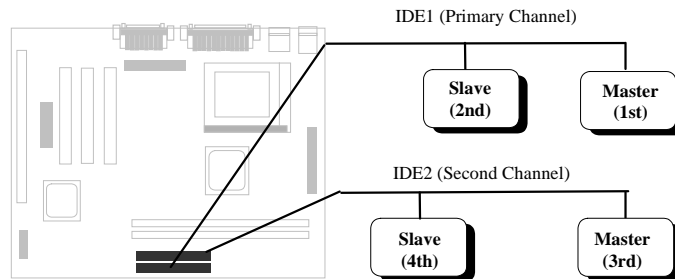
×êË_Ë'İP%Qç<0a0~İnMmaster mode ÄYËİÄ IDE1»RİP%Kç<0a0~İnMslave
mode Äa0aEİÄ IDE1»TÄfÄXË'ÄİİP%çç<%çİPçç<»R×êÄÄÄEİÄÄ IDE2 Ä0 master
%ç slave mode»T



×ê%f%ü: IDE İhÈ Ä0PİÈä×^İæÄ %4çZĐhÓ] 46 %Ä%Ü (18Ço
Äe)»RçYÄ\Ö ÈaĐaÜ %4Ä”»T

×ê%f%ü: ÆËÖWÄ İæÄeÄ0ÄY00Ä•× »RËä×^İæÖNÖ=Ä00a0~İæÄ€
İnÄÄ master mode»RAYÄæNi %F0éÄ0PİÄ0ĐDÄaÄŞÖaÑ†0a0~»T

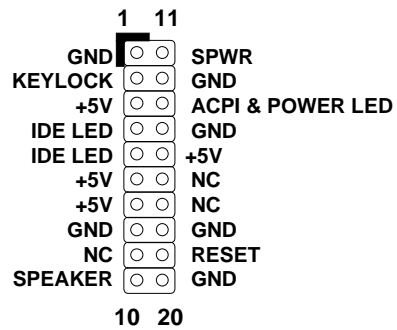
İŞB ÅŠ0à



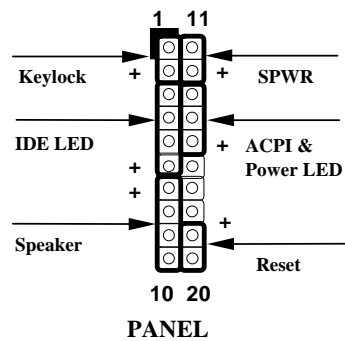
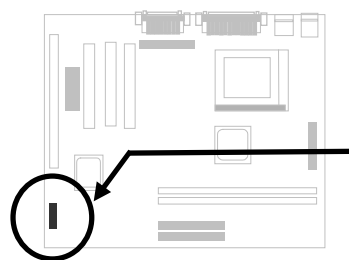
2.3.11 Åv¼ ÇĖÄ`ĖİÜj

Åv¼ ÇĖÄ`ĖİÜj 20-pin ĖàĖŠ»R0ëžöÀÅ
PANEL»TÀ0Ėä×`ĖİÜj žACPI & Power
LED Å žöÜ` »RŪp×]Ū (keylock)»RÇĀŅ†
Đ"Ø (reset) Å Đ†»RÍü ž' (speaker)
İž»TĖ' žžYĀæž|0éĀiÅŠ0à»T

Çj Ė' Āi Ās BIOS ¼¼ĪnĀŠ "suspend
 mode"»RĀyŅ Đz¼ suspend Ōi Ā»Ėā»R
 ACPI & Power LED Å žöÜ` Ā\ŅĐ"Āj
 Ė'Ÿ' »T



PANEL



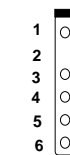
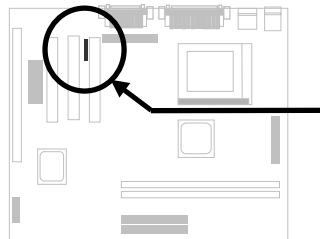
İŞB ÅŠ0à

2.3.12 Å ħ•×^ĐáÙ Ê (IrDA)

ĥŌĥU0 Å`ÄŌİP%XA`ÀTÊ (serial port 2) ĥŲİİ ĤrDA Å ĥ•×^Ōiİi»TĀiŪn ĤrDA (Infrared Data Association) Ĥ ĥŌÆ ĥë HP»SCompaq»SIBM İĥŪfĀi ĀĀĥùĀŌ%Q ĤiİiŪİ»RĥëĀiĒŪŌtŌSĥëÆ ĥ•×^Đá×^Ō ÈaĀŌĀŌİ_ŌaŪİĥë»TĀĥĀiİĒP ĀİŌiĒiĥİ ĥİ%R ĤrDA Ā\İ%ĀSĀeĀĀÆ ĥ•×^ĐáÙ ĀŌŌeNā»Tĥ^ĤĒÈ'ĀŌŌ„Ō%ĀŷİāÆ ĥ•×^ĐáÙ ĥm Ēú»RĥSİBĀi ĤrDA İĤĀŠ»RĬ_ĒúĒ Ās%QĀŠĐkŪ %Ō»R%ŲŌ÷ĤĒİ†Ēİ×^%ÆĤİ†Ēİ»RĀĵ ĥĀŌĒāŌāİĀĒ`ĀĤŌ„Ō%ĀeĤi%YŌāĀ Āfİ' (PDA) ĤŲ%Ĥİ†×^»SĐaŌŌŪāĒnŌ ÈaĀeĒ_ %Ųĥ ĒvĀ %İİ ĤrDA ĀŌĀĴ Ā Ō ĀTĀĴ»TĥŌĥU0 Å`ĥŲİİĀHPSİR (115Kbps, 1 meter) Ōa ĀSK-İR (56Kbps) İĥİhĒ »T

ĀŠŌāĒā»R×eĒ ĤrDA Å ĥ•×^ŌiİiĀŠİ»Ā ĥU Ō Ā`%ĤİŌeĀİİrDA ĀŌĒāĒŠ»TĀŠŌā%ĀĀ»RĒ' ŪŌĥİŌ÷Đ"ĒiİSZ dİdv†fİ%ŌĀŌÆ ĥ•×^ĥmŪ»R % ĥĥŪĒq%ĀQ»T

Pin	Description
1	+5V
2	NC
3	İRRX
4	GND
5	İRTX
6	NC

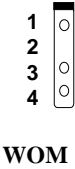
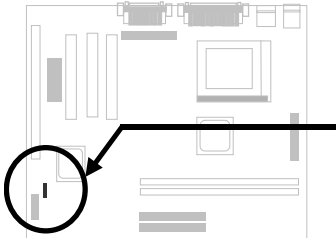


IrDA

2.3.13 Wake On Modem ĖİÚj

çõçU0 Ä` %hÄyÄÍÉdÈ x`ð Ínçf»Rçz%pİÄÖa060 ÕÖĖĖĐ"0
(0V Wake On Modem) çġú»R%ÖĖİÄ» (AOpen MP56) Äè
ç•ĖİÄ»Öa060 Ä»çØRçè»TçèÄ Äèçè%ÖĖİÄ»Öa06çuÄÖÖö»RçÇ
Ėä%ÄÖ÷ĖİĐ` Ó„Ñx»RÄiçYÄöÇæ% ð ÄoPİĖ'Äèçè»TÇj Ė'ĖPçèÄÖ
Æ AOpen MP56»RÄyxeÄèçè 4-pin ÍġĖİx`»RÍġĖİ MP56
ÄÖ RING ĖİÚj ÖaçU0 Ä` %hÄWOM ĖİÚj »T

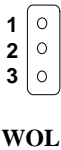
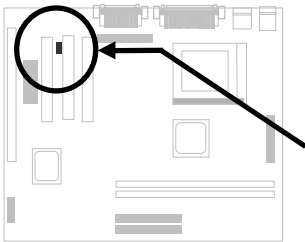
Pin	Description
1	+5V SB
2	NC
3	RING
4	GND



2.3.14 Wake On LAN ĖİÚj

çõçU0 Ä` ÄyÄÍWOL ĖİÚj »R ÇĖÄèçèWake On LAN çm
Ėú»RçİĐÑÑĖİçpİÄÄÖçġüÄÖÖ ð çuÖaÖ ÖöİĖß »T

Pin	Description
1	+5V SB
2	GND
3	LID

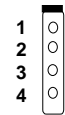
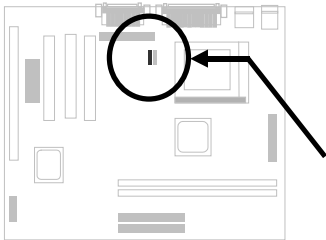


İŞB AŞÖà

2.3.15 CD ÇİÑ××^ËİÜj

İ, ÇİËİÜj Æ ÇèÂİİ†Ëİ CDROM ÄÖÇİÑ××^»T

Pin	Description
1	L
2	GND
3	GND
4	R

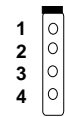
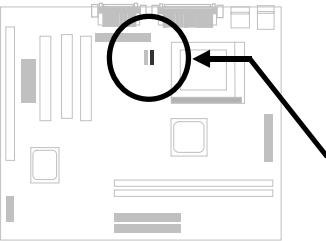


CD-IN

2.3.16 Mono In/Mic Out ËİÜj

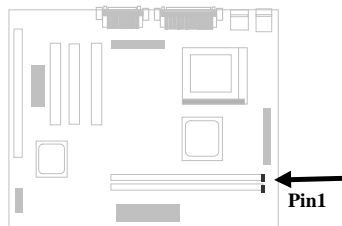
İ, ÇİËİÜj ÇZèÂİİ†Ëİ%ÖËİÀ»Öa06ÇuÄÖ Mono In/Mic Out ËİÜj »TÄp%»Pin 1-2 Æ Mono In»Rpin 3-4 Æ Mic Out»TÇÈÄqÑ_ÄÖÆ »RÇöÄvİ, %QY ÄÖËİÜj ÄYÄdÄİÄe ÄSA00eNa»RÇ^Äİ%00aÄ00a06ÇuÄİİ^Äeİ, ÇİËİÜj »T Ü ÜaÄv×eÇ Êe0m|¼ ÄÖËİÖ"ÄŞÖ,,»RÄYÄgÖa06ÇuÄÖ0İËİ ÖuÊ÷İ^ Ñ»»T

Pin	Description
1	Mono In
2	GND
3	GND
4	Mic Out



MODEM-CN

2.4 ÅŠòà:UE`Øêß



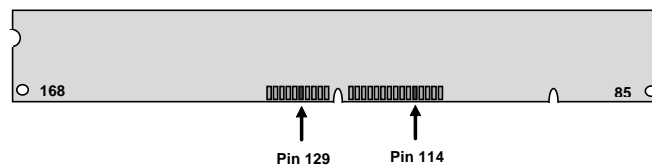
ÀÓ çUØ Ä` ÅÍ 2 È DIMM (Dual-in-line Memory Module) Î»Öë»RçZY»pÎÄ SDRAM (Synchronous DRAM)»R ÍæÄÄ ÈvĐ„ çzÖWÄ 256MB»T

çÖçUØ Ä` çZY»pÎÄÖëÑ64bit ÄÖ DIMM ÖiÎi»T

- I. ¼¼: Í ÇÈÆ 1Mx64 (8MB)»S2Mx64 (16MB)»S4Mx64 (32MB)»S8Mx64 (64MB)»S16Mx64 (128MB)»WÀi Ü ÇÈÆ 1Mx64x2 (16MB)»S2Mx64x2 (32MB)»S4Mx64x2 (64MB)»S8Mx64x2 (128MB)»T



Î½ö: ÅÍÇi¼ Ä|çZYÜaÆÈ' ÄÖ DIMM Æ Í ÇÈÜöÆ Ü ÇÈ -- ÆÆDIMM ¼hÇÄÖ pin 114 Öa pin 129»RÀfÄX ÅÍÊ†Ö-Ö„Ö »RÍ, È DIMM çzÉúÎ_Æ Ü ÇÄÖ»WApÄy Î_Æ Í ÇÄÖ»T×eÈëNi¼ÇÄÖÖöè»T

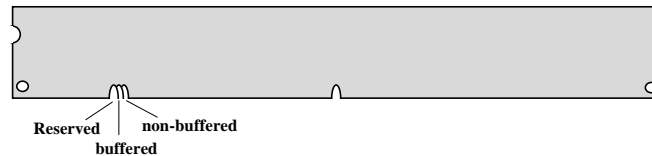


II. Speed:

¼QÉ Æ ÖëçöÀf-12 Í, ÖöÄÄ»»RÍ, Ä çöÀ†Ä Í†Äñ (clock cycle time) Æ 12ns»RÄiçY ÀÖ SDRAM Íæ¼ÄÖ clock Æ 83MHz»TÜöÅÍç¼QÖöëçöÆ ÀfAa-67 Í, ÖöÄÄ»»RÄ çöÄaÄSÖ ÈaDäÜ ÍæöçZÄ 67MHz»T

İŞB AŞÖà

III. **Buffered òa non-buffered:** ÇÖÇUØ Ä` ¼pİÄnon-buffered DIMM»TË' ÇÇYÄæðð
DIMM ¼hÇËËä¼ÄÖÀ Ò~»RÄíÄäÄ non-buffered DIMM òa buffered DIMM»T×è
ËèÑí¼f ÖèÄi Çö»X



ÇèÄ Èä¼ÄÖÀ Ò~¼Äa»RÇ`Äí non-buffered DIMM ÇÇYÏ»¼ÇUØ Ä` ¼hÄÖDIMM
Ï»Öè»TÛ Ì^ ÇòÄvÇÄË¼hÆ÷Ä ÄÖDIMM Í¼SÖxÆ non-buffered ÄÖ¼WRAöÇæ¼Äö
PÍË'ÄsÜ ÜaÈäÍaæÜöÆ Ò ÄËËròüË÷Ì^Ñ»»T

IV. **2-clock òa 4-clock signals:** Û Ì^ 2-clock òa 4-clock ÄÖ DIMM Í¼ÇÇYÇèÄsÍ,
ð ÇUØ Ä` ¼h»RÄ Æ¼WÄÏèYÇÄSÄaÏöÑb»RAöÇæ¼ÄöPÍË' ÍaæÄèÇ4-clock ÄÖ
SDRAM»T



Ì¼ö: ÇÇÜaÄuË' ÄÖ SDRAM Æ 2-clock ÜöÆ 4-clock
ÄÖ»RÇÇYÆ÷Æ÷ pin 79 òa pin 163»RAfÄXÄÍË±Ö-Ö,Ö
Ï_ÜÍÖiÆ 4-clock»WpÄyÄ\Æ 2-clock ÄÖ»T

V. ÄaÄ ¼Ö: ¼pİÄÖèÑaÄÖ 64 bit wide (Ì] parity) SDRAM»T

VI. ¼pİÄ **SPD:** BIOS N'ÄöÈäÈÖÏ ÄÍ SPD ÄÖ DIMM»RÄYÄöÈäÍnÄSÖRN ÄÖtiming»T
ÄdÄÍ SPD ÄÖ DIMM ÄsÍ, ð ÇUØ Ä` ¼hÜöÆ ÇÇYÄèÇè»RÄBIOS POST ÈäÑBy
Çö¼QÇíÌ¼öèÈä»RÄDUË' ÄèÇèÄÖDIMM ÄdÄÍ¼pİÄ SPD»T

BIOS ÇÄöÈäÈÖÏ È` Øèß ÄÖÈvð,¼èÄÄ»R¼Ö÷ÄèÇè Jumper ÍnÄS»TÍa¼ÄÖÈ` Øèß
Èvð,Æ 256MB»T

Total Memory Size = Size of DIMM1 + Size of DIMM2

İŞB ÅŠòà

¿Y%FÀT¿ÀoPÍÂé¿èÃÖ DRAM İiAi »X

DIMM Data chip	Bit size per side	Single/ Double side	Chip count	DIMM size	Recommended
1M by 16	1Mx64	x1	4	8MB	Yes
1M by 16	1Mx64	x2	8	16MB	Yes
2M by 8	2Mx64	x1	8	16MB	Yes
2M by 8	2Mx64	x2	16	32MB	Yes
4M by 16	4Mx64	x1	4	32MB	Yes
4M by 16	4Mx64	x2	8	64MB	Yes
8M by 8	8Mx64	x1	8	64MB	Yes
8M by 8	8Mx64	x2	16	128MB	Yes

DIMM Data chip	Bit size per side	Single/ Double side	Chip count	DIMM size	Recommended
2M by 32	2Mx64	x1	2	16MB	Yes, but not tested.
2M by 32	2Mx64	x2	4	32MB	Yes, but not tested.

¿Y%FÀT¿ÀoPÍÂé¿èÃÖ DRAM İiAi »X

DIMM Data chip	Bit size per side	Single/ Double side	Chip count	DIMM size	Recommended
4M by 4	4Mx64	x1	16	32MB	No
4M by 4	4Mx64	x2	32	64MB	No
16M by 4	16Mx64	x1	16	128MB	No