

## Addendum 2

1. **Contents page v:** Delete line 3-9.
2. **Contents page viii:** Delete figures 3-6 and 3-7.
3. **Page 1-2:** Line “L2 Write-back cache support (0K, 128K, 256K, or 512K)” should be “L2 Write-back cache support (0K, 128K, or 256K)”.
4. **Page 2-2:** Replace Figure 2-1 M4Li System Board with page 2 of this addendum.
5. **Page 2-5:** Delete 512K cache jumper setting from Table 2-4.
6. **Page 2-6:** Change default setting for Jumper W28 (Table 2-6) from 1-2 to 2-3. Change default setting for Jumper W29 (Table 2-6) from 1-2 to 2-3.
7. **Page 2-6:** Change default setting for Jumper W33 to closed for both PS/2 and AT versions.
8. **Page 3-9:** Delete entire page.

## M4Li Components

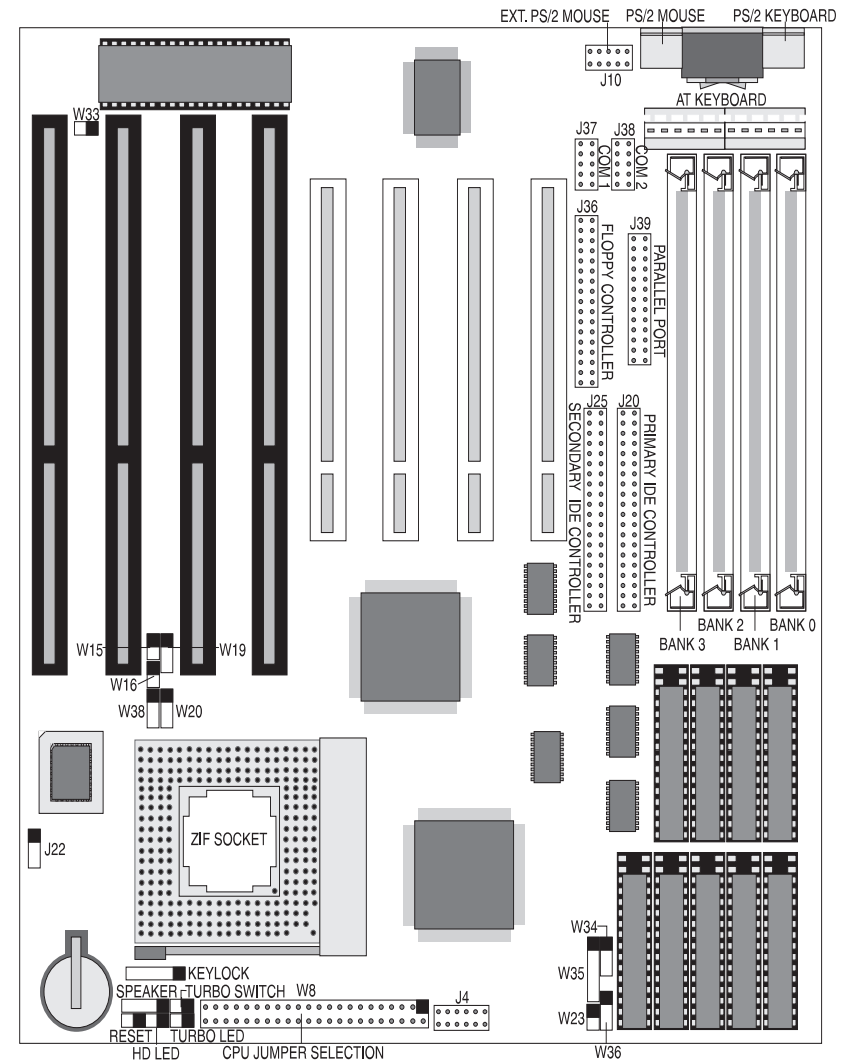


Figure 2-1 M4Li System Board

*Note:*

*ISA Slot 4 and PCI Slot 1 are shared. Only one slot may be used at a time.*

*PCI slot 1 is not a bus-mastering slot.*