JUMPER	486SX/P23S*/ Cx486S (M6) (PGA)	Am486DXL P24S*/P4S*/ 486DX/ IntelDX2™/ IntelDX4™/ Cx486DX (M7)/ Cx486S+Cx487S (M6+C6) (PGA)	P24T*/P24CT (PGA)	P24D* (PGA)
JC1	2-3	1-2	1-2	1-2
JC2	2-3	1-2	1-2	1-2
JC3	open	open	short	open
JC4	1-2	1-2	2-3	2-3

* P23S, P24S, P24D, and P4S are the SL-enhanced CPUs while P24T is the Overdrive Processor.

→ **#NOTE : When the onboard 3.3 volt regulator is not present, the 3.3 volt daughter board should be installed. If not, please refer to page 2-12 installation of the 3.3 volt regulator daughter board.**

JUMPER (RP 0Ω 8P4R)	486SX/DX/ IntelDX2™	P23S/P4S/ P24S	Cx486S/DX	P24D/ IntelDX4™/ P24CT	Am486DXL
JC5	open	short	open	short	open
RC1	empty	empty	empty	inserted	empty
RC2	empty	empty	inserted	empty	empty
RC3*	empty	empty	empty	empty	inserted

*There are two options of VIA 82C486A on RC3 : 1235 and 1272. If 1235 is selected, RC3 performs its function. If 1272 is chosen, RC3 will not work.

JUMPER	PIN DEFINITION	
J2, J10, JX5	1-2	Regular CPU
	2-3	SL-enhanced CPU
JC6	IntelDX4™ Internal Clock Select	
	Open	Internal 3 X (default)
	1-2	Internal 2.5 X
	2-3	Internal 2 X
JC7	P24T Write-back/Write-through Select	
	Short	Write-back
	Open	Write-through
JX3	1-2	Intel SL-enhanced CPU
	2-3	Cyrix CPU, AMD