

JUMPER	486SX/ Cx486S (M6)	486DX/IntelDX2™ IntelDX4™ Cx486DX (M7)	487SX/P24CT* P24T*
JC2	2-3	1-2	1-2
JC3	2-3	1-2	1-2
JC4	open	1-2	2-3

* P24T and P24CT are the Pentium OverDrive Processors.

JUMPER (RP 0Ω SIP)	Am486DXL	IntelDX4™ SL-enhanced/ P24T	Cx486S/DX	486DX
JRN1	inserted	empty	empty	empty
JRN2	empty	inserted	empty	empty
JRN3	empty	empty	inserted	empty

→ **Note :** When the 3.3 volt regulator is not present, the 3.3 volt daughter board should be installed. If not, please read page 2-6 "3.3 Volt Regulator Board Installation".

JUMPER	PIN DEFINITION	
JT1	1-2	Cyrix DX
	2-3	Cyrix DX2
JT2	1-2	IntelDX4™ 3 X mode
	2-3	IntelDX4™ 2 X mode
JT3	1-2	P24T internal write-back cache
	2-3	P24T internal write-through cache
JT4	1-2	Other CPUs
	2-3	P24T
JN1, JN2	Short Open	P24D (Pentium OverDrive Processor) Other CPUs
JT5*	1-2	A26 (default)
	2-3	For card that uses A31
JT6	1-2	Address area is programmed in VT82C505 chip
	2-3	Supports IRQ15 when SMI# is used
JT7, JT8, JT9 JT11	1-2	(factory default)

*Only one jumper cap is allowed, either JT5 or JT6. Cannot short both jumpers at the same time.

Table 2-1. Jumper Settings for CPU Selector (Continued)