

Typematic Delay (Msec)

Sets the delay time before a character is repeated.

The options are: 250 (Default), 500, 750, 1000 millisecond.

Security Option

Allows you to set the security level when booting up the system.

The available options are: Setup (Default), System.

Video BIOS Shadow

Allows the BIOS to copy the video ROM code of the add-on video card to the system memory for faster access.

The options are: Enabled (Default), Disabled.

C8000-CBFFF to DC000-DFFFF Shadow

Allows the BIOS to copy the BIOS ROM code of the add-on card to system memory for faster access. It may improve the performance of the add-on card.

Some add-on cards will not function properly if its BIOS ROM code is shadowed. To use these options correctly, you need to know the memory address range used by the BIOS ROM of each add-on card.

The available options are: Enabled, Disabled (Default).

Chipset Features Setup

ROM PCI/ISA BIOS (2A51.7F09) CHIPSSET FEATURES SETUP			
Decoupled Refresh	: Enabled	DRAM for BANK 0	: Standard
Video BIOS Cacheable	: Enabled	DRAM for BANK 1	: Standard
System BIOS Cacheable	: Enabled	DRAM for BANK 2	: Standard
Memory Hole At 15MB Addr.	: Disabled	Onboard FDC Control	: Enabled
Cache Timing Control	: Fast	Onboard Serial Port 1	: COM1 / 38400
SRAM Tag/Alt Bit Config.	: 7 Tags + ALT	Onboard Serial Port 2	: 375H/1607
IDE HDD Bus Mode	: Enabled	Onboard Printer Mode	: Standard
Onboard IDE First Channel	: Enabled	ECP Use DMA Channel No.:	3
Onboard IDE Second Channel	: Enabled		
1st Channel IDE Master PIO	: Auto	ESC: Quit	: 1.1 ~ *
1st Channel IDE Slave PIO	: Auto	F5: Old Values	: PU/PD/-/ : Modify
		F6: Load BIOS Defaults	: (Shift) F2 : Color
		F7 : Load Setup Defaults	

Decoupled Refresh

When enabled, the onboard DRAM will be decoupled from ISA bus memory device so that the processor can re-access the onboard DRAM without waiting for the completion of ISA bus memory refresh.

Disable this if you are using the ISA type ET-4000 VGA card.

The available options are: Enabled (Default), Disabled.

Video BIOS Cacheable

When enabled, allows the system to use the video BIOS code from the cache instead of the slower DRAMs or ROMs.

The available options are: Enabled (Default), Disabled.

System BIOS Cacheable

When enabled, allows the ROM area F000H-FFFFH cacheable as cache controller is enabled.

The available options are: Enabled (Default), Disabled.